

#6/election  
7/2/03  
Jame Th

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANT: GLENN G. DAVIES, ET AL. ) Group Art Unit: 2811  
SERIAL NO. 09/989,666 )  
FILED: November 20, 2001 ) Examiner: Vu, H.  
TITLE: STRUCTURE AND METHOD FOR ) Confirmation No. 1815  
WIRING TRANSLATION BETWEEN )  
GRIDS WITH NON-INTEGRAL PITCH )  
RATIOS IN CHIP CARRIER MODULES )

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Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA. 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

In response to the restriction requirement mailed on May 30, 2003, and in accordance with the provisions under 37 CFR § 1.115 and 1.143, the Applicants submit the following election for further prosecution on the merits:

Election:

The Applicants hereby provisionally elect Group I, claims 1-14, directed to a semiconductor device, classified in Class 257, subclass 786.

FIS920010103US1  
125-0004

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**REMARKS**

The requirement for restriction being the only issue raised in the present Office Action, this paper is fully responsive to the same. The Applicants hereby reserve the right to file a divisional application for any non-elected subject matter in this application.

In the event the Examiner has any queries regarding the presently submitted response, applicants' attorney respectfully requests the courtesy of a telephone conference to discuss any matters in need of attention. If there are any associated or additional charges with respect to this Response or otherwise, please charge them to Deposit Account No. 09-0458 maintained by the Applicant's Assignee.

Respectfully submitted,

GLENN G. DAVIES, ET AL.

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Applicants' Attorneys

By: 

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|---|--------------------------------|--------------------------|-------------------------------|--|
| <b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>  |                                |                          | Docket No.                    |  |
| Applicant(s): <b>GLENN G. DAVIES ET AL.</b>   |                                |                          | <b>FIS920010103US1</b>        |  |
| Serial No.<br><b>09/989,666</b>   | Filing Date<br><b>11/20/01</b> | Examiner<br><b>H. YU</b> | Group Art Unit<br><b>2811</b> |  |
| Invention: <b>STRUCTURE AND METHOD FOR WIRING TRANSLATION BETWEEN GRIDS WITH<br/>NON-INTEGRAL PITCH RATIOS IN CHIP CARRIER MODULES</b>  |                                |                          |                               |  |
| <p>I hereby certify that this <u>Response to Restriction Requirement</u><br/>(Identify type of correspondence)</p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9318</u>)</p> <p>on <u>June 27, 2003</u><br/>(Date)</p> <p style="text-align: right;"><u>Jessica L. Walsh</u><br/>(Typed or Printed Name of Person Signing Certificate)</p> <p style="text-align: right;"><u>Jessica L. Walsh</u><br/>(Signature)</p> <p style="text-align: center;">Note: Each paper must have its own certificate of mailing.</p> <p style="text-align: right;"><b>FAX RECEIVED</b><br/><b>JUN 27 2003</b><br/><b>TECHNOLOGY CENTER 2800</b></p> |                                |                          |                               |  |

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